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<u>L22</u>	(work or temp\$6 or general) near3 register\$1 near8 l1	4837	<u>L22</u>
<u>L21</u>	l20 and l4	143	<u>L21</u>
<u>L20</u>	(work or temp\$6 or general) near3 register\$1 near15 l1	5709	<u>L20</u>
<u>L19</u>	L17 and l5	1668	<u>L19</u>
<u>L18</u>	L17 and l4	144	<u>L18</u>
<u>L17</u>	(work or temp\$6 or general) near3 register\$1 near30 l1	6035	<u>L17</u>
<u>L16</u>	L14 and l5	1681	<u>L16</u>
<u>L15</u>	L14 and l4	144	<u>L15</u>
<u>L14</u>	(work or temp\$6 or general) near3 register\$1 near70 l1	6095	<u>L14</u>
<u>L13</u>	L12 and l4	298	<u>L13</u>

<u>L12</u> (work or temp\$6 or general) near3 register\$1 and l1	16215	<u>L12</u>
<u>L11</u> (work or temp\$6 or general) near3 register\$1	23235	<u>L11</u>
<u>L10</u> L9 and fetch\$5	25063	<u>L10</u>
<u>L9</u> l1 and (decod\$3)	159675	<u>L9</u>
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<u>L8</u> l3 and l5	8	<u>L8</u>
<u>L7</u> l3 and l4	0	<u>L7</u>
<u>L6</u> l1 and l4	4930	<u>L6</u>
<u>L5</u> (712/202-300)![CCLS]	8025	<u>L5</u>
<u>L4</u> (717/104-143)![CCLS]	5499	<u>L4</u>
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<u>L3</u> L1 and (2\$push\$4 or 2\$pop\$4)	33	<u>L3</u>
<u>L2</u> L1 near25 (2\$push\$4 or 2\$pop\$4)	0	<u>L2</u>
(transfer\$6 or mov\$3) near15 register\$1 near15 (memor\$4 or stack) near15		
<u>L1</u> (one or single) (microinstruction or command\$3 or instruction or macro or macroinstruction\$1)	1105968	<u>L1</u>

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28. Impact of imperfect link adaptation in EGPRS
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29. Cycle-accurate macro-models for RT-level power analysis
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